

ABSTRACT OF THE DISCLOSURE

A synchronizing circuit synchronizes a predetermined code with first and second codes different in phase, include: a code generating part outputting  
5 phase-shifted code shifted in phase by a predetermined number of chips from the predetermined code; a first correlation detecting part detecting a correlation between the phase-shifted code from the code generating part and the first code; a second correlation detecting part  
10 detecting a correlation between the phase-shifted code from the code generating part and the second code; and a code shifting part shifting the phase of the phase-shifted code from the code generating part by a predetermined number of chips according to the detection results of the  
15 first and second correlation detecting parts.